SOFL+MSVL 2015 Workshop Program (Final)
6 November 2015
Paris, France

9:00 Opening Address

9:10 ~ 10:00 Invited Talk (Chair: Shaoying Liu)
Genericity in PAR Platform
Jinyun Xue

10:00 ~ 10:20 Coffee break

10:20 ~ 11:20 Modeling and Specification (Chair: Cong Tian)
Modeling and Verification of an Interrupt System in µC/OS-II with TMSVL
Yuqin Zhou, Jin Cui, Zhenhua Duan, Cong Tian and Nan Zhang

The Interchange Format of Tabular Expressions Using XML
Mao Huang, Yihai Chen, Ridha Khedri and Huaikou Miao

A GUI-Aided Approach to Formal Specification Construction
Shaoying Liu

11:20 ~ 12:20 Testing and Debugging (Chair: Huaikou Miao)
Automatic Generation of Specification-based Test Cases by Applying Genetic Algorithms in Reinforcement Learning
Yuji Sato and Taku Sugihara

Fault Localization of Timed Automata using Maximum Satisfiability
Shin Nakajima and Si-Mohamed Lamraoui

Automated Program Debugging for Multiple Bugs Based on Semantic Analysis
Aishan Liu

12:20 ~ 13:30 Lunch

13:40 ~ 15:00 Model Checking and Verification (Chair: Shin Nakajima)
Model Checking Process Scheduling over Multi-core Computer System with MSVL
Xinfeng Shu and Zhenhua Duan

A Method Based on MSVL for Verification of the Social Network Privacy Policy
Xiaobing Wang, Tao Sun and Zhenhua Duan

A Case Study: SOFL + Model Checking for OSEK/VDX Application
Zhuo Cheng, Zhang Haitao, Yasuo Tan and Yuto Lim

On Reachability Problem of Updatable Timed Automata with One Updatable Clock
Yunqing Wen, Guoqiang Li and Shoji Yuen
15:00 ~ 15:30 Coffee break

15:40 ~ 17:00 Algorithm and Transformation (Chair: Zhenhua Duan)
Research on Formal Development of Non-recursive Algorithm of Graph Search
Qimin Hu, Jinyun Xue and Zhen You

LtlNfBa: Making LTL Translation More Practical
Cong Tian and Jun Song

Transforming PPTL Formula to BA and A SPIN Based Model Checker for PPTL
Xiaoming Zhang

Automatic Transformation from SOFL Module Specifications to Program Structures
Xiongwen Luo and Shaoying Liu

17:00 ~ 17:05 Closing